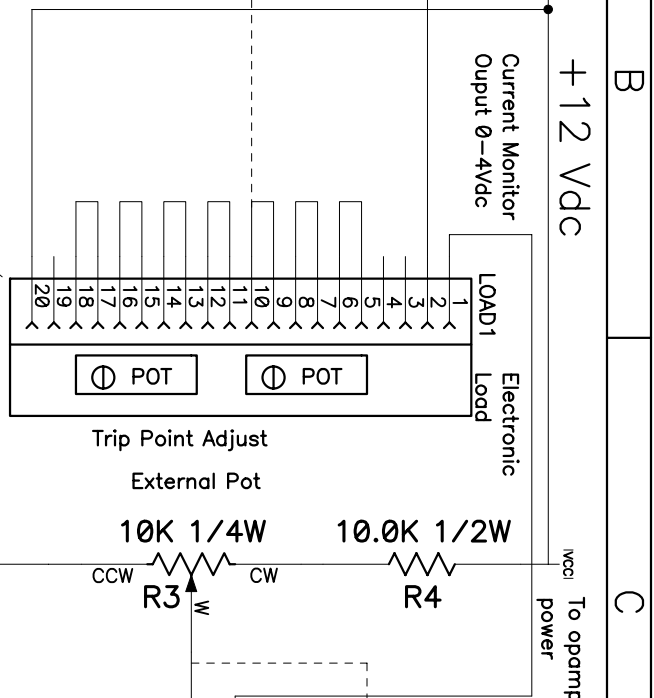


Optional D to A Input 0-4 Vdc Pin 10

GNDANALOG  
Analog / Bias Return



Scaled output of current  
0-100% = 0-4Vdc

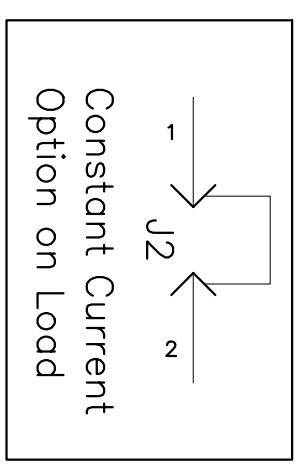
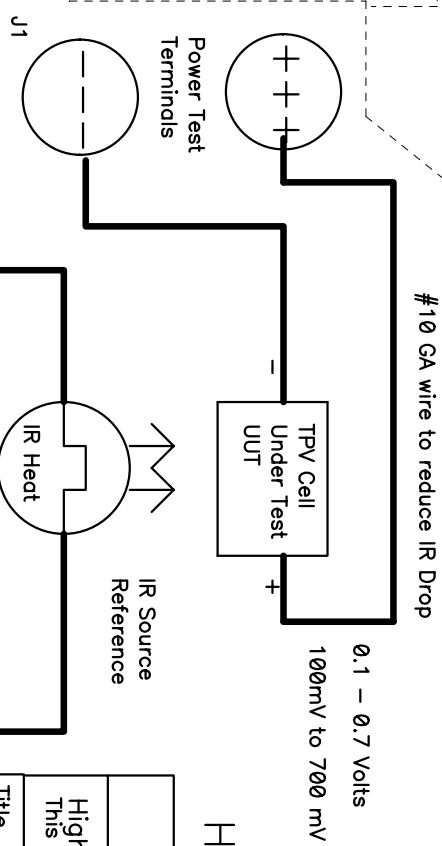
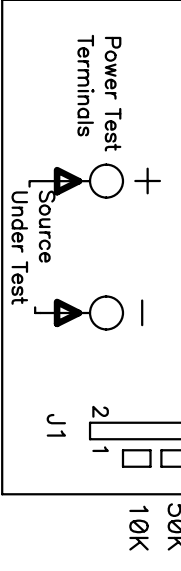
Optional D to A Input 0-5 Vdc

Good / Bad  
Go / No Go  
Output / Driver 0 or 12Vdc

Connector shown from top view.  
Pins shown as seen on electronic load.

Note J1 - Pin 2 and the (-) source under test are connected, never have current flowing through this path. All control signals should be connected to J1-pin 2. Only the source under test should be connected to the power test terminal (+) and (-).

Viewed from top of load



Optional historesis resistor  
J2 open for Constant Current  
Load must have option 2

[Http://www.EXEC-ENG.com](http://www.EXEC-ENG.com)

Executive Engineering

High Volume Solar Cell Tester for Manufacturing  
This is a go / no-go tester using a fixed light source

Title Solar Cell Testing using constant current

Size	Number	Rev
A	301/151 Solar Cell	1

Date	Mon Sep 20, 2004	Drawn by	dw
Filename	Solar-TPV-testing.SCH	Sheet	1 of 1