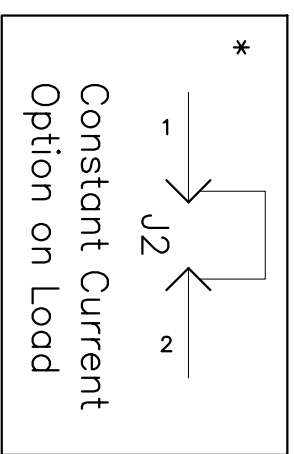
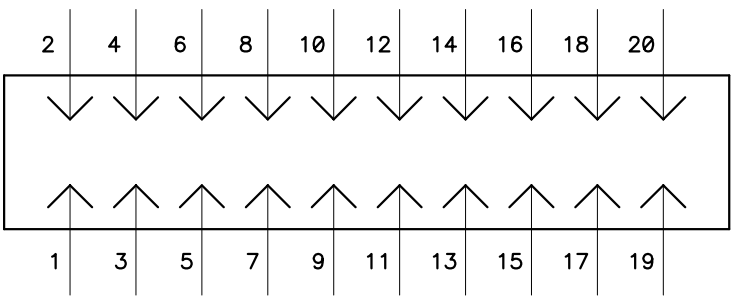
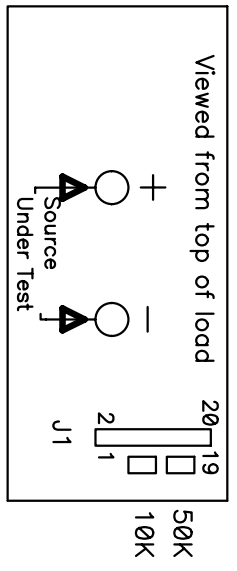


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Connector shown from top view. Pins shown as seen on electronic load.

Note J1 - Pin 2 and the (-) source under test are connected, never have current flowing through this path. All control signals should be connected to J1-pin 2. Only the source under test should be connected to the power test terminal (+) and (-).



Use Cap across terminal to stop oscillation of electronic loads when using long connection lead to loads.

EXECUTIVE ENGINEERING

Title Fixed Current Control

Size	Number	Rev
A	EE301/151	A
Date	Drawn by	Sheet
6-15-2000	D. WEBER	1 of 1
Filename	fix-res.sch	